EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	85	(DUT or (device adj under adj test$3)) with asynchron$5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 18:03
L5	23	(DUT or (device adj under adj test$3)) with asynchron$5 with clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 18:03
L8	2	"20050081113".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:20
L9	14035	((device or unit) adj under adj test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L10	1902	stimulus adj signal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L11	321	L9 and L10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L12	466	synchroniz$5 with (DUT or (device adj under adj test))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L13	0	synchroniz$5 with (DUT or (device adj under adj test)) with buffer with wirte with read	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28

EAST Search History

L14	10	synchroniz$5 with (DUT or (device adj under adj test)) with buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L15	130	synchroniz$5 with (DUT or (device adj under adj test)) and buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L16	61	synchroniz$5 with (DUT or (device adj under adj test)) and buffer and write and read	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L17	1921	375/224	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L18	212	synchronization with (DUT or (device adj under adj test))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L19	3	L17 and L18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L20	3	L12 and L17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L21	8	synchronization with (DUT or (device adj under adj test)) and (clock with domain)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28

EAST Search History

L22	239	"26010"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L23	7	"97/26010"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L24	2	"6161160".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L25	2	"5381420".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L26	42	("4430735" | "4584683").PN. OR ("4893072").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 20:28
L27	15	test with pcm with modem	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 20:28
L28	146	(synchroniz$5 with clock) with (DUT or (device adj under adj test))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L29	0	("2002/0141525").URPN.	USPAT	OR	ON	2006/05/03 20:28
L30	15	("4918652" | "5081601" | "5363319" | "5603015" | "5678028" | "5732247" | "5768567" | "5790829" | "5848236" | "5905883" | "5987243" | "6028996" | "6099579" | "6115823" | "6233540").PN. OR ("6879948"). URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 20:28
L31	1	"4534030".pn.	US-PGPUB; USPAT; USOCR	OR	ON	2006/05/03 20:28

EAST Search History

L32	2	"5499248".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28
L33	2	"5453995".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/05/03 20:28

PALM Intranet

Application Number SEARCH

IDS Flag Clearance for Application 10032513

IDS Information

Content	Mailroom Date	Entry Number	IDS Review	Reviewer
M844	10-26-2001	9	☑	05-01-2002 13:26:07 dsavoy

UPDATE

PALM INTRANET

Day : Wednesday
Date: 5/3/2006
Time: 13:02:30

Inventor Information for 10/032513

Inventor Name	City	State/Country
BEHRENS, KLAUS-PETER	GAERTRINGEN	GERMANY
ROTTACKER, MARKUS	STUGGART	GERMANY
MOHR, JOERG-WALTER	EUTINGEN	GERMANY

Appln Info | Contents | Petition Info | Atty/Agent Info | Continuity Data | Foreign Data | Inver

Search Another: Application# [ ] Search **or Patent#** [ ] Search

PCT / [ ] / [ ] Search **or PG PUBS #** [ ] Search

Attorney Docket # [ ] Search

Bar Code # [ ] Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

PALM INTRANET

Day : Wednesday
Date: 5/3/2006
Time: 13:02:34

Inventor Name Search Result

Your Search was:

Last Name = BEHRENS
First Name = KLAUS-PETER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07958224	Not Issued	166	10/08/1992	APPARATUS FOR GENERATING TEST SIGNALS	BEHRENS, KLAUS-PETER
08189200	5499248	150	01/31/1994	TEST VECTOR GENERATOR COMPRISING A DECOMPRESSION CONTROL UNIT AND A CONDITIONAL VECTOR PROCESSING UNIT AND METHOD FOR GENERATING A TEST VECTOR	BEHRENS, KLAUS-PETER
08306723	5453995	150	09/15/1994	APPARATUS FOR GENERATING TEST SIGNALS	BEHRENS, KLAUS-PETER
10032513	Not Issued	71	10/26/2001	Data flow synchronization	BEHRENS, KLAUS-PETER
10816646	Not Issued	41	04/02/2004	Parameterized signal conditioning	BEHRENS, KLAUS-PETER
11234447	Not Issued	25	09/23/2005	Source synchronous sampling	BEHRENS, KLAUS-PETER

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
BEHRENS	KLAUS-PETER	Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

PALM INTRANET

Day : Wednesday
Date: 5/3/2006
Time: 13:02:58

Inventor Name Search Result

Your Search was:

Last Name = ROTTACKER
First Name = MARKUS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10032513	Not Issued	71	10/26/2001	Data flow synchronization	ROTTACKER, MARKUS
10816646	Not Issued	41	04/02/2004	Parameterized signal conditioning	ROTTACKER, MARKUS
11117968	Not Issued	30	04/29/2005	Pin coupler for an integrated circuit tester	ROTTACKER, MARKUS
11234447	Not Issued	25	09/23/2005	Source synchronous sampling	ROTTACKER, MARKUS

Inventor Search Completed: No Records to Display.

Search Another: Inventor **Last Name** ROTTACKER **First Name** MARKUS Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

PALM INTRANET

Day : Wednesday
Date: 5/3/2006
Time: 13:03:16

Inventor Name Search Result

Your Search was:

Last Name = MOHR
First Name = JOERG-WALTER

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10032513	Not Issued	71	10/26/2001	Data flow synchronization	MOHR, JOERG-WALTER
11353662	Not Issued	30	02/14/2006	Testing a device under test by sampling its clock and data signal	MOHR, JOERG-WALTER

Inventor Search Completed: No Records to Display.

Search Another: Inventor **Last Name** MOHR **First Name** JOERG-WALTER Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

Web

Results 1 - 8 of 8 for "asynchronous clock domains" dut. (0.60 seconds)

Tip: Try removing quotes from your search to get more results.

DeepChip Homepage
This gives the system the ability to emulate true > **asynchronous clock domains**. ...
translation from the C model/testbench to the **DUT** inside the emulator. ...
www.deepchip.com/items/0450-10.html

[PDF] The Most Cost Effective Accelerated Verification Solutions
File Format: PDF/Adobe Acrobat
... e Code **DUT** e Celerator Synthesizeable HDL Celaro ... Interactive debug for hardware and software with full visibility True **Asynchronous clock domains** support ...
mentorkr.com/board/data_file/ pds_docu/(6)MED_korea_seminar_f.pdf

[PDF] SOC Prototyping for
File Format: PDF/Adobe Acrobat - View as HTML
... Characteristics of In-Circuit Emulation Mode Multiple **asynchronous clock domains** may exist ... Debug the design by probing any **DUT** signals in simulator Page 20. ...
www.ap-soc.com/tech/Prototype_ Method_Session/AP-SOC(SY%20Yang).pdf

Method and apparatus for analyzing digital circuits patent invention
... more **asynchronous clock domains**, wherein clock domain crossing signals are temporarily stored and supplied to a second **DUT** in a time-delayed manner; and ...
www.freshpatents.com/ Method-and-apparatus-for-analyzing-digital-circuits-dt20050414ptan20050081113.php

> ### Method and apparatus for analyzing digital circuits patent invention
> ... of a digital circuit design including two or more **asynchronous clock domains**, ... The temporal evolution, that is, the electrical behavior of a **DUT**, ...
> www.freshpatents.com/ Method-and-apparatus-for-analyzing-digital-circuits-dt20050414ptan20050081113.php?ty...

TestCompany mail: 0-In Release V2.1j is available today
..., report redundant bits of a multi-bit signal crossing **asynchronous clock domains** ... of a create_assign is used In both a checker directive and The **DUT** ...
testcompany.com/until-nov2004/2199.html

[PDF] The IEEE 1355 Standard: Developments, Performance and Application ...
File Format: PDF/Adobe Acrobat - View as HTML
DUT. Grounding connection ... synchronisation between these two **asynchronous clock domains**. Figure 29: TS-FO Interface VHDL Code Structure. LINKCNTL ...
atlas.web.cern.ch/Atlas/project/ cern/ep-atr/download/pubdoc/thesis/haas_thesis.pdf

[PDF] DesignWare IP Family Reference Guide
File Format: PDF/Adobe Acrobat
SWITCH (**DUT**). Port 2. Port - Rx. Port - Tx. Port 16. Port - Rx. Port - Tx ... **Asynchronous clock domains** support. different PHY frequencies. This ...
www.synopsys.com/products/designware/ docs/doc/dwf/manuals/dw_qrg.pdf

"asynchronous clock domains" dut Search

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

Google Home - Advertising Programs - Business Solutions - About Google

SCIRUS
for scientific information only

"asynchronous clock domains" AND dut Search

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Sorry, your search has not produced any results. Before searching again, using the same or similar keywords, you may find it helpful to:

- **check the selected sources, information types and subject areas, the selection may not contain results matching your query**
- **check the spelling of all words**
- **spell words in a different way, for example using American spelling**
- **write abbreviations and acronyms in full**
- **use alternative words that have the same meaning**
- **search using fewer or more general words**

SCIRUS
for scientific information only

asynchronous AND clock AND domains AND dut Search

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Searched for:: :All of the words:**asynchronous** AND **clock** AND **domains** AND **dut**

Found:: :**2 total | 0 journal results | 0 preferred web results | 2 other web results**

Sort by:: :**relevance | date**

Save checked results | Email checked results | Export checked results

☐ **1.** Thank you for your interest in the International Technology Roadmap for Semiconductors : [PDF-930K]
Jun 2000
Welcome ! Thank you for your interest in the International Technology Roadmap for Semiconductors: 1999 edition. We have worked diligently as teams of Technology Working Groups from around the world to assess technology requirements for the semiconductor industry over the next 15 years.
[http://www.ra.informatik.uni-stuttgart.de/~rainer/Lite...]
similar results

☐ **2.** No Title [PDF-459K]
May 1999
PRELIMINARY A Intel 450KX/GX PCIset The information in this document is subject to change. PRELIMINARY A Information in this document is provided in connection with Intel products.
[http://www.intel.com/design/chipsets/datashts/29052301...]
similar results

Your query was rewritten to asynchronous AND "clock domains" AND dut
We did this by adding quotes to common phrases and by removing non-essential words.
- Repeat without rewrite

Or refine using:
All of the words
Refine

fast

Results for "((asynchronous<in>metadata) <and> (clock domains<in>metadata))<and> (..."

Your search matched **1** of **1344704** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

1. **Eliminating non-determinism during test of high-speed source synchronous differ buses**
   Mohanram, K.; Touba, N.A.;
   VLSI Test Symposium, 2003. Proceedings. 21st
   27 April-1 May 2003 Page(s):121 - 127
   AbstractPlus | Full Text: PDF(347 KB) **IEEE CNF**
   Rights and Permissions